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FACSIMILE COVER SHEET

DATE:

October 12, 2004

TO:

USPTO - Official Central Fax

FACSIMILE NO.:

703-872-9306

TELEPHONE:

FROM:

Robert W. Holland

Hamilton & Terrile, LLP

SUBJECT:

USSN 10/615,678

Attorney Docket No. DC-05229

Customer No.: 33438

This transmittal consists of 11 page(s), including this cover sheet.

MESSAGE:

Enclosed for filing in USSN 10/615,678, please find a Response to Final Office Action (9 pages) and Submission of Corrected Drawings (1 page) and Figure 1 Replacement Sheet (1 Sheet). Thank you.

If you do not receive all pages, please call (512) 338-9100.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Jason J. Lee, James D. Curlee

Assignee:

Dell Products L.P.

Title:

System and Method for Housing a Signal Connector of a Printed Circuit

Board

Serial No.:

10/615,678

Filing Date:

July 8, 2003

Examiner:

Chandrika Prasad

Group Art Unit:

2839

Docket No.:

DC-05229

Customer No.:

33438

Austin, Texas October 12, 2004

Attn. Official Draftsperson COMMISSIONER FOR PATENTS PO BOX 1450 ALEXANDRIA, VA 22313-1450

SUBMISSION OF CORRECTED DRAWINGS

Dear Sir:

Applicants submit one (1) sheet of corrected drawings, consisting of Figure 1, in the above-named application. The Figures have been corrected as shown on the attached sheets with strikeouts and underlines. If there are any questions regarding these drawings, please call the undersigned at (512) 338-9100.

I hereby certify that this correspondence is being sent via facsimile to the USPTO Central Facsimile on October 12, 2004.

Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

Robert W. Holland

Attorney for Applicant(s)

Reg. No. 40,020